Abstract

A fiber optic connection panel including a plurality of circuits. The circuits are accessed through termination locations. Preferably, IN and OUT termination locations are located on a first side of the panel during normal through operation. A switch included in the circuitry disconnects the IN and OUT circuit paths on the first side, and connects each of the termination locations to further termination locations, located on an opposite side of the panel in a patched state. Preferably, at least one of the circuit paths includes a monitor circuit. A chassis is provided including a plurality of circuit modules mounted to the chassis, wherein the circuit modules contain circuitry with exposed termination locations wherein the circuitry allows normal through and patched circuit paths selectively linking different termination locations.

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